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(Not for submission under 37 CFR 1.99)

Application Number	10550326
Filing Date	2005-09-23
First Named Inventor	Roger D. Chamberlain
Art Unit	2439
Examiner Name	Tolentino, Roderick
Attorney Docket Number	53047-57365

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	5	6625150		2003-09-23	Yu	
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9	7496108		2009-02-24	Biran et al.	
10	7558925		2009-07-07	Bouchard et al.	
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	4	20040111632		2004-06-10	Halperin	
	5	20050135608		2005-06-23	Zheng	

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	6	20050229254		2005-10-13	Singh et al.	
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	3	11306268	JP	A	1999-11-05	Shibata Sunao et al.		<input type="checkbox"/>
	4	11316765	JP	A	1999-11-16	Nippon Steel Corp		<input type="checkbox"/>
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	6	2002101089	JP	A	2002-04-05	Mitsubishi Corp		<input type="checkbox"/>
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	4	KOLONIARI ET AL., "Content-Based Routing of Path Queries in Peer-to-Peer Systems", pp. 1-19, E. Bertino et al. (Eds.): EDBT 2004, LNCS 2992, pp. 29-47, 2004, copyright by Springer-Verlag, Germany	<input type="checkbox"/>
	5	LI ET AL., "Large-Scale IP Traceback in High-Speed Internet: Practical Techniques and Theoretical Foundation", Proceedings of the 2004 IEEE Symposium on Security and Privacy, 2004, pp. 1-15	<input type="checkbox"/>
	6	Office Action for CA Application 2522862 dated May 3, 2010	<input type="checkbox"/>
	7	Office Action for EP Application 04752943.3 dated April 26, 2010	<input type="checkbox"/>
	8	Office Action for JP Application 2006-514919 dated August 24, 2010	<input type="checkbox"/>
	9	SEKI ET AL., "High Speed Computation of Shogi With FPGA", Proceedings of 58th Convention Architecture, Software Science, Engineering, March 9, 1999, pp. 1-133-1-134	<input type="checkbox"/>

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YOSHITANI ET AL., "Performance Evaluation of Parallel Volume Rendering Machine Re Volver/C40", Study Report of Information Processing Society, March 5, 1999, pp. 79-84, Vol. 99, No. 21

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